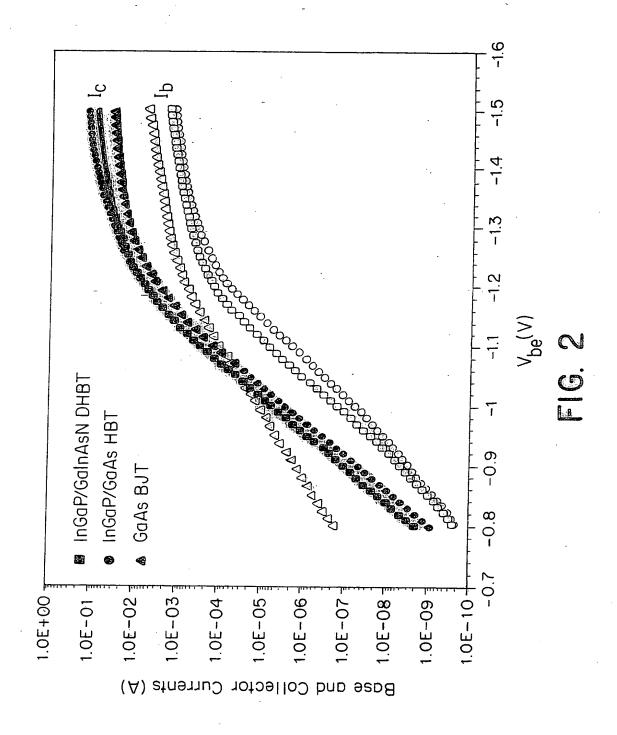
	500 Å Si-doped In _{0.6} Ga _{0.4} As (1 x 10 ¹⁹ cm ⁻³)	•
	500 Å Si-doped InGaAs Grade (1×10 ¹⁹ cm ⁻³)	
·	1500 Å Si-doped GaAs (5 x 10 ¹⁸ cm ⁻³)	
	500 Å Si-doped InGaP (4 x 10 ¹⁷ cm ⁻³)	
500-	1800 Å C-doped Ga _{1-x} In _x As _{1-y} N _y (1.5-4.5 x 10 ¹ x≈3y	¹⁹ cm ⁻³)
	7500 Å Si-doped GaAs (1x10 ¹⁶ cm ⁻³)	
_	5000 Å Si-doped GaAs (5 x 10 ¹⁸ cm ⁻³)	
	GaAs SUBSTRATE	

FIG. I

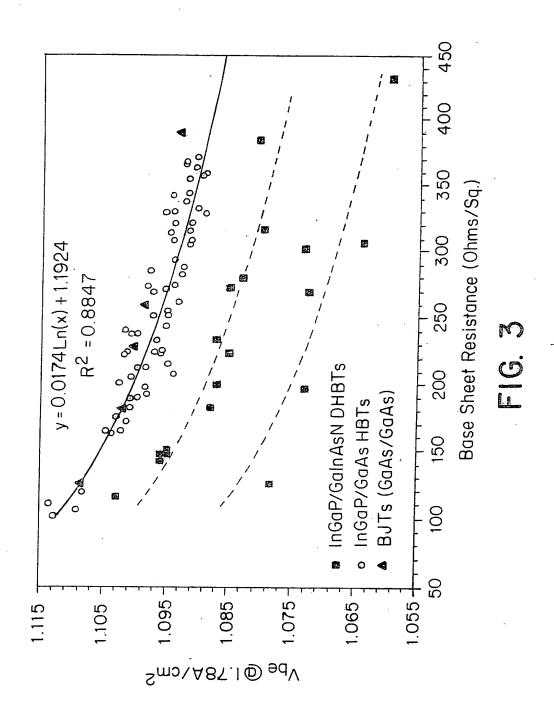
Docket No.: 0717.2013-013

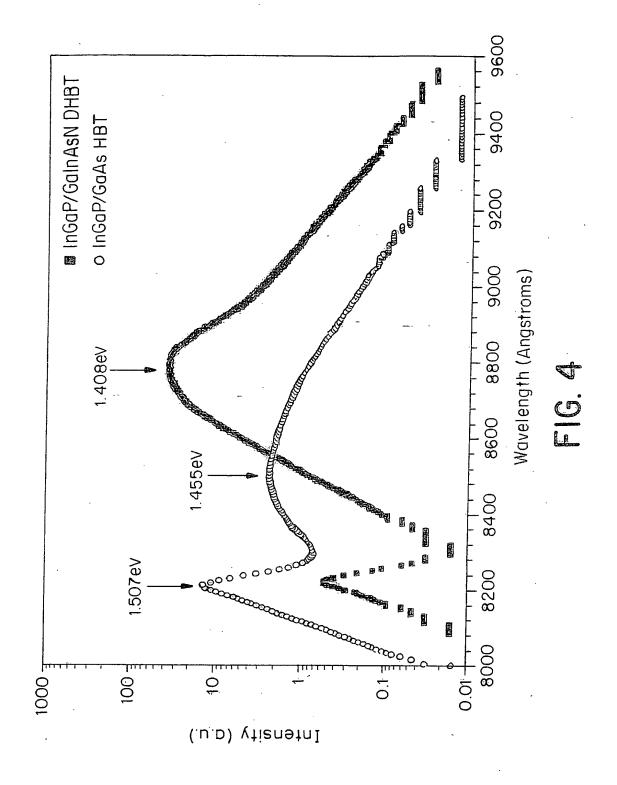
Title: Bipolar Transistor with Lattice... Inventors: Roger E. Welser *et al*.

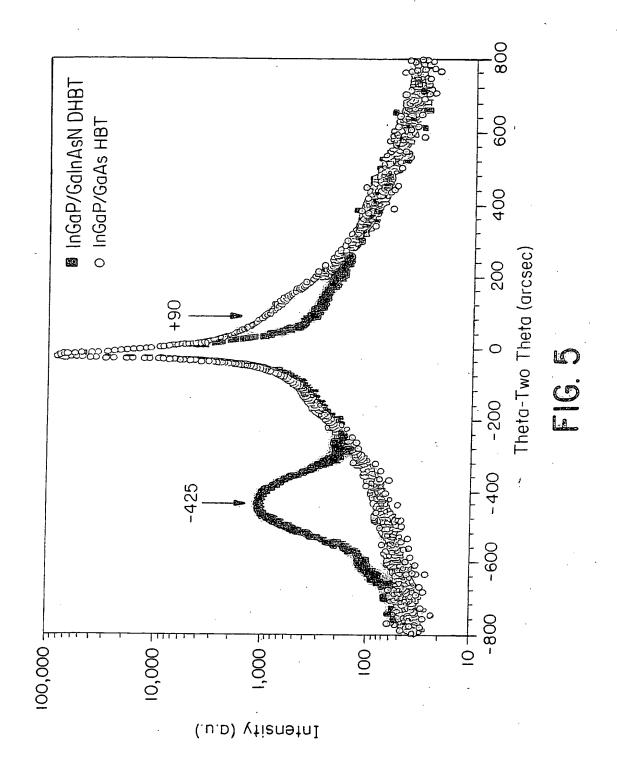


Docket No.: 0717.2013-013

Title: Bipolar Transistor with Lattice... Inventors: Roger E. Welser *et al.*

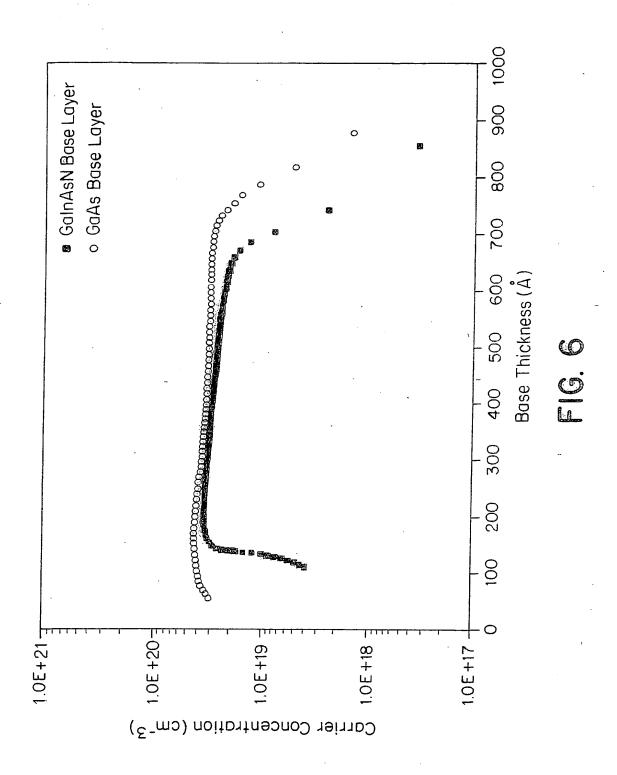






Docket No.: 0717.2013-013

Title: Bipolar Transistor with Lattice...
Inventors: Roger E. Welser et al.



	·	
	500Å Si-doped In _{0.6} Ga _{0.4} As(1x10 ¹⁹ cm ⁻³)	
	500 Å Si-doped InGaAs Grade (1×10 ¹⁹ cm ⁻³)	
	1500 Å Si-doped GaAs (5 x 10 ¹⁸ cm ⁻³)	
	500 Å Si-doped InGaP (4×10 ¹⁷ cm ⁻³)	
	50 Å transitional layer	
500	O Å C-doped-Ga _{1-×} In _× As _{1-y} N _y (1.5-4.5 x 10 ¹⁹ cr x ≈3y compositional grade	m ⁻³
-	50 Å transitional layer	
	∼200 Å Si-doped InGaP	
	4000 Å Si-doped GaAs (1 x 10 ¹⁶ cm ⁻³)	
	5000 Å Si-doped GaAs (5 x 10 ¹⁸ cm ⁻³)	
,	GaAs SUBSTRATE	

FIG. 7